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Day : Friday
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Inventor Name Search Result

Your Search was:

Last Name = REHG
First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name 29
60154385	Not Issued	159	09/16/1999	A METHOD FOR EFFICIENTLY REGISTERING OBJECT MODELS IN IMAGES VIA DYNAMIC ORDERING OF FEATURES	REHG, JAMES M.
60154384	Not Issued	159	09/16/1999	METHOD FOR LEARNING SWITCHING LINER SYSTEM DYNAMIC MODELS FROM DATA	REHG, JAMES M.
60131748	Not Issued	159	04/30/1999	SCHEDULING CONSTRAINED DYNAMIC APPLICATIONS FOR PARALLEL TARGETS	REHG, JAMES M.
10663938	Not Issued	094	09/16/2003	METHOD FOR MOTION CLASSIFICATION USING SWITCHING LINEAR DYNAMIC SYSTEM MODELS	REHG, JAMES MATTHEW
10662067	Not Issued	094	09/12/2003	METHOD FOR VISUAL TRACKING USING SWITCHING LINEAR DYNAMIC SYSTEM MODELS	REHG, JAMES MATTHEW
09997419	Not Issued	077	11/29/2001	WIRELESS MULTI-USER MULTI-PROJECTOR PRESENTATION SYSTEM	REHG, JAMES M.
09662474	Not Issued	040	09/15/2000	METHOD AND SYSTEM FOR CORRELATING DATA STREAMS	REHG, JAMES M.
09654426	6591146	150	09/01/2000	METHOD FOR LEARNING SWITCHING LINEAR DYNAMIC SYSTEM MODELS FROM DATA	REHG, JAMES MATTHEW
09654401	Not Issued	041	09/01/2000	METHOD FOR MOTION SYNTHESIS AND INTERPOLATION USING SWITCHING LINEAR	REHG, JAMES MATTHEW

				DYNAMIC SYSTEM MODELS	
09654300	6694044	150	09/01/2000	METHOD FOR MOTION CLASSIFICATION USING SWITCHING LINEAR DYNAMIC SYSTEM MODELS	REHG, JAMES MATTHEW
09654022	6683968	150	09/01/2000	METHOD FOR VISUAL TRACKING USING SWITCHING LINEAR DYNAMIC SYSTEM MODELS	REHG, JAMES MATTHEW
09574866	Not Issued	071	05/19/2000	ON-LINE SCHEDULING OF CONSTRAINED DYNAMIC APPLICATIONS FOR PARALLEL TARGETS	REHG, JAMES M.
09565414	6795567	150	05/05/2000	METHOD FOR EFFICIENTLY TRACKING OBJECT MODELS IN VIDEO SEQUENCES VIA DYNAMIC ORDERING OF FEATURES	REHG, JAMES MATTHEW
09549351	Not Issued	071	04/14/2000	SCHEDULING CONSTRAINED DYNAMIC APPLICATIONS FOR PARALLEL TARGETS	REHG, JAMES M.
09466975	6618490	150	12/20/1999	METHOD FOR EFFICIENTLY REGISTERING OBJECT MODELS IN IMAGES VIA DYNAMIC ORDERING OF FEATURES	REHG, JAMES MATTHEW
09466970	6597801	150	12/20/1999	METHOD FOR OBJECT REGISTRATION VIA SELECTION OF MODELS WITH DYNAMICALLY ORDERED FEATURES	REHG, JAMES MATTHEW
09185280	6314204	150	11/03/1998	MULTIPLE MODE PROBABILITY DENSITY ESTIMATION WITH APPLICATION TO MULTIPLE HYPOTHESIS TRACKING	REHG, JAMES MATTHEW
09185279	6226409	150	11/03/1998	MULTIPLE MODE PROBABILITY DENSITY ESTIMATION WITH APPLICATION TO SEQUENTIAL MARKOVIAN DECISION PROCESSES	REHG, JAMES MATTHEW
09185278	6353679	150	11/03/1998	SAMPLE REFINEMENT METHOD OF MULTIPLE MODE PROBABILITY DENSITY ESTIMATION	REHG, JAMES MATTHEW

09086032	6675189	150	05/28/1998	SYSTEM FOR LEARNING AND APPLYING INTEGRATED TASK AND DATA PARALLEL STRATEGIES IN DYNAMIC APPLICATIONS	REHG, JAMES MATHEW
09085795	6480876	150	05/28/1998	SYSTEM FOR INTEGRATING TASK AND DATA PARALLELISM IN DYNAMIC APPLICATIONS	REHG, JAMES MATHEW
09059651	6269172	150	04/13/1998	METHOD FOR TRACKING THE MOTION OF A 3-D FIGURE	REHG, JAMES MATHEW
09059478	6240198	150	04/13/1998	METHOD FOR FIGURE TRACKING USING 2-D REGISTRATION	REHG, JAMES MATTHEW
09059197	6243106	150	04/13/1998	METHOD FOR FIGURE TRACKING USING 2-D RIGISTRATION AND 3-D RECONSTRUCTION	REHG, JAMES MATTHEW
09059194	6256418	150	04/13/1998	METHOD AND SYSTEM FOR COMPRESSING A SEQUENCE OF IMAGES INCLUDING A MOVING FIGURE	REHG, JAMES MATTHEW
09039022	6266068	150	03/13/1998	MULTI-LAYER IMAGE-BASED RENDERING FOR VIDEO SYNTHESIS	REHG, JAMES M.
08909405	6067604	150	08/11/1997	SPACE-TIME MEMORY	REHG, JAMES M.
08876603	5930379	150	06/16/1997	METHOD FOR DETECTING HUMAN BODY MOTION IN FRAMES OF A VIDEO SEQUENCE	REHG, JAMES M.
08844444	6256046	150	04/18/1997	METHOD AND APPARATUS FOR VISUAL SENSING OF HUMANS FOR ACTIVE PUBLIC INTERFACES	REHG, JAMES M.

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = KNOBE
First Name = KATHLEEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 6
60131748	Not Issued	159	04/30/1999	SCHEDULING CONSTRAINED DYNAMIC APPLICATIONS FOR PARALLEL TARGETS	KNOBE, KATHLEEN
10629357	Not Issued	030	07/29/2003	DEAD TIMESTAMP IDENTIFICATION AND ELIMINATION	KNOBE, KATHLEEN
09574866	Not Issued	071	05/19/2000	ON-LINE SCHEDULING OF CONSTRAINED DYNAMIC APPLICATIONS FOR PARALLEL TARGETS	KNOBE, KATHLEEN
09549351	Not Issued	071	04/14/2000	SCHEDULING CONSTRAINED DYNAMIC APPLICATIONS FOR PARALLEL TARGETS	KNOBE, KATHLEEN
09086032	6675189	150	05/28/1998	SYSTEM FOR LEARNING AND APPLYING INTEGRATED TASK AND DATA PARALLEL STRATEGIES IN DYNAMIC APPLICATIONS	KNOBE, KATHLEEN
09085795	6480876	150	05/28/1998	SYSTEM FOR INTEGRATING TASK AND DATA PARALLELISM IN DYNAMIC APPLICATIONS	KNOBE, KATHLEEN

Inventor Search Completed: No Records to Display.

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	("6675189" "6480876" "5930379").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:08
L2	54	((schedul$3 near5 (cost$3 optim$5 perform$5 static$4 dynamic$4)) same ((application program software task$3 process$3) near5 (perform$5 run$4 execut$5 operat$3))) and (schedul$3 with static$4 with ((cost$3 near4 low$3) optim$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:10
L3	8	L2 and "718"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:10
L4	5	L2 and 718/102.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:10
L5	3	L2 and 718/104.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:10
L6	2	L2 and 712/208.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:11
L7	2	L2 and 712/214.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:11
L8	122	(schedul$3 with (estimat$3 predict$3) with cost with (task$3 process$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:11
L9	122	(schedul$3 with (estimat$3 predict$3) with cost with (task$3 process$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:12

L10	8	L9 and "718"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:12
L11	5	L9 and 718/102.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:12
L12	7	L9 and 718/104.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:12
L13	0	L9 and 712/208.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:12
L14	0	L9 and 712/214.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:12

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41 A SMART scheduler for multimedia applications
Jason Nieh, Monica S. Lam
May 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 2
Full text available: pdf(570.87 KB) Additional Information: f

Real-time applications such as multimedia audio and video are increasingly populating the worksta created SMART, a Scheduler for Multimedia And Real-Time applications. SMART supports applicati the support for real-time applications is integrat ...

Keywords: Scheduling, multimedia, proportional sharing, real-time

42 Eliminating synchronization bottlenecks using adaptive replication
Martin C. Rinard, Pedro C. Diniz
May 2003 **ACM Transactions on Programming Languages and Systems (TOPLA**
Full text available: pdf(826.28 KB) Additional Information: f

This article presents a new technique, adaptive replication, for automatically eliminating synchroni multiple threads attempt to concurrently update the same object. It is often possible to eliminate : without interacting with other threads. When ...

Keywords: Atomic operations, commutativity analysis, parallel computing, parallelizing compilers

43 Wireless home networks: Design and implementation of the HiperLan/2 protocol
E. P. Vasilakopoulou, G. E. Karastergios, G. D. Papadopoulos
April 2003 **ACM SIGMOBILE Mobile Computing and Communications Review**, Vc
Full text available: pdf(1.50 MB) Additional Information: f

In recent years, wireless communication systems have experienced an enormous development, le as their coverage, data rates, mobility and QoS support. Among them the HiperLan/2 standard is Control protocol though is the most critical and complex funct ...

44 Code optimization - I: Compiler optimization-space exploration
Spyridon Triantafyllis, Manish Vachharajani, Neil Vachharajani, David I. August
March 2003 **Proceedings of the international symposium on Code generation an**

Full text available: pdf(1.19 MB) Publisher Site Additional Information: f

To meet the demands of modern architectures, optimizing compilers must incorporate an ever larg interfere with subsequent transformations, compilers employ predictive heuristics to guide optimiz today's wide-issue machines severely limit the accura ...

45 Design and development of data-intensive web sites: The Araneus approach
Paolo Merialdo, Paolo Atzeni, Giansalvatore Mecca
February 2003 **ACM Transactions on Internet Technology (TOIT)**, Volume 3 Issue 1

Full text available: pdf(2.18 MB) Additional Information: f

Data-intensive Web sites are large sites based on a back-end database, with a fairly complex hype composed of a set of steps and design transformations that lead from a conceptual specification of implementation process, by allowing the ...

Keywords: Databases, Internet, WWW, World Wide Web, development

46 Run-time adaptation in river
Remzi H. Arpaci-Dusseau
February 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 1

Full text available: pdf(849.04 KB) Additional Information: f

We present the design, implementation, and evaluation of run-time adaptation within the River da processing applications to cope with performance variations that are common in cluster platforms. analysis, we answer four previously unanswered and important que ...

Keywords: Performance availability, clusters, parallel I/O, performance faults, robust performanc

47 What next?: A dozen information-technology research goals
Jim Gray
January 2003 **Journal of the ACM (JACM)**, Volume 50 Issue 1

Full text available: pdf(1.18 MB) Additional Information: full citation, references, citings, index terms

48 Multithreading II: A quantitative framework for automated pre-execution thread selection
Amir Roth, Gurindar S. Sohi
November 2002 **Proceedings of the 35th annual ACM/IEEE international symposium**

Full text available: pdf(2.12 MB) Publisher Site Additional Information: f

Pre-execution attacks cache misses for which address prediction driven prefetching fails. In pre-ex threads whenever the processor anticipates an upcoming miss. P-thread selection is the task of de minimized. It is central to the success of pre-execution.We intr ...

49 A decoupled scheduling approach for the GrADS program development environment
Holly Dail, Henri Casanova, Fran Berman
November 2002 **Proceedings of the 2002 ACM/IEEE conference on Supercomputing**

Full text available: pdf(153.55 KB) Additional Information: f

Program development environments are instrumental in providing users with easy and efficient ac HPC systems, there are currently no widely used environments for Grid programming. The goal of execution facilities for Grid program development.In this pape ...

50 Executing multiple pipelined data analysis operations in the grid
Matthew Spencer, Renato Ferreira, Michael Beynon, Tahsin Kurc, Umit Catalyurek, Alan Sussman, Jo
November 2002 **Proceedings of the 2002 ACM/IEEE conference on Supercomputing**

Full text available: pdf(158.51 KB) Additional Information: f

Processing of data in many data analysis applications can be represented as an acyclic, coarse gra which is represented as a pipelined chain of processing on data. We define the scheduling problem presented using a visualization application.

51 MPICH-V: toward a scalable fault tolerant MPI for volatile nodes
George Bosilca, Aurelien Bouteiller, Franck Cappello, Samir Djilali, Gilles Fedak, Cecile Germain, Thoı
November 2002 **Proceedings of the 2002 ACM/IEEE conference on Supercomputing**

Full text available: pdf(204.28 KB) Additional Information: f

Global Computing platforms, large scale clusters and future TeraGRID systems gather thousands (Volatility reduces the MTBF of the whole system in the range of hours or minutes.We present MPI(logging. MPICH-V architecture relies on Channel Memories, C ...

52 Online feedback-directed optimization of Java
Matthew Arnold, Michael Hind, Barbara G. Ryder
November 2002 **ACM SIGPLAN Notices , Proceedings of the 17th ACM SIGPLAN conf**

Full text available: pdf(463.00 KB) Additional Information: f

This paper describes the implementation of an online feedback-directed optimization system. The sampling framework to collect control flow graph edge profiles. This profile information is used to (splitting. We empirically evaluate this syst ...

Keywords: adaptive optimization, dynamic optimization, online algorithms, virtual machines

53 Invited talk: Managing dynamic concurrent tasks in embedded real-time multimedia systems
Peng Yang, Paul Marchal, Chun Wong, Stefaan Himpe, Francky Catthoor, Patrick David, Johan Vounc
October 2002 **Proceedings of the 15th international symposium on System Synthe**

Full text available: pdf(675.04 KB) Additional Information: f

This paper addresses the problem of mapping an application, which is highly dynamic in the future purpose. By exploring the Pareto curves and scenarios generated at design time, the run-time sch consumption. A real-life example from a 3D quality of service ...

Keywords: embedded system, low-power, multiprocessor, scheduling

54 ECOSystem: managing energy as a first class operating system resource
Heng Zeng, Carla S. Ellis, Alvin R. Lebeck, Amin Vahdat
October 2002 **Proceedings of the 10th international conference on Architectural s**

Full text available: pdf(1.17 MB) Additional Information: f

Energy consumption has recently been widely recognized as a major challenge of computer syster system nature, presents challenges beyond those of conventional resource management. To meet fair allocation of available energy among applications. Our par ...

55 Automatic performance setting for dynamic voltage scaling
Krisztián Flautner, Steve Reinhardt, Trevor Mudge
September 2002 **Wireless Networks**, Volume 8 Issue 5

Full text available: pdf(328.69 KB) Additional Information: f

The emphasis on processors that are both low power and high performance has resulted in the inc power use and performance, provided there is a mechanism in the OS to control that tradeoff. In t use. Our mechanism is implemented in the Linux ke ...

Keywords: dynamic voltage scaling, interactive performance, performance-setting, power manag

56 Reconfigurable computing: a survey of systems and software
Katherine Compton, Scott Hauck
June 2002 **ACM Computing Surveys (CSUR)**, Volume 34 Issue 2

Full text available: pdf(710.56 KB) Additional Information: f

Due to its potential to greatly accelerate a wide variety of applications, reconfigurable computing h performance, while retaining much of the flexibility of a software solution. In this survey, we explc internal structures and external coupling. W ...

Keywords: Automatic design, FPGA, field-programmable, manual design, reconfigurable architect

57 Software for Reconfigurable Systems: Analysis of quasi-static scheduling techniques in a vir
Yury Markovskiy, Eylon Caspi, Randy Huang, Joseph Yeh, Michael Chu, John Wawrzynek, André DeH
February 2002 **Proceedings of the 2002 ACM/SIGDA tenth international symposiun**

Full text available: pdf(243.54 KB) Additional Information: f

The SCORE compute model uses fixed-size, virtual compute and memory pages connected by stre of physical compute pages is smaller than the number of virtual compute pages in the abstract co automatic scheduler that selects the temporal sequencing of ...

58 Energy efficient architectures: Exploiting VLIW schedule slacks for dynamic and leakage en
W. Zhang, N. Vijaykrishnan, M. Kandemir, M. J. Irwin, D. Duarte, Y-F. Tsai
December 2001 **Proceedings of the 34th annual ACM/IEEE international symposium**

Full text available: pdf(1.19 MB) Publisher Site Additional Information: f

The mobile computing device market is projected to grow 16.8 million units in 2004, representing to forefront. As circuits continue to scale in future, it would important to optimize both leakage an spanning from circuit to software levels.Sched ...

59 A case for dynamic view management
Yannis Kotidis, Nick Roussopoulos
December 2001 **ACM Transactions on Database Systems (TODS)**, Volume 26 Issue 4

Full text available: pdf(892.57 KB) Additional Information: f

Materialized aggregate views represent a set of redundant entities in a data warehouse that are fr different profiles of the users who submit queries, there is need for tools that will automate and ea collections of materialized aggregate views in a data warehous ...

Keywords: Data cube, OLAP, data warehousing, materialized views

60 Session 1: Perspectives on software evolution 1: Evolution in software and related areas
M. M. Lehman, J. F. Ramil
September 2001 **Proceedings of the 4th International Workshop on Principles of Soft**

Full text available: pdf(1.68 MB) Additional Information: f

After briefly discussing the meaning of the term *evolution* in the context of software, its technolog

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61 Computing curricula 2001
September 2001 **Journal on Educational Resources in Computing (JERIC)**
Full text available: pdf(613.63 KB) html(2.78 KB) Additional Information: full citation, references, citings, ind

62 Parallel execution of prolog programs: a survey
Gopal Gupta, Enrico Pontelli, Khayri A.M. Ali, Mats Carlsson, Manuel V. Hermenegildo
July 2001 **ACM Transactions on Programming Languages and Systems (TOPLA**
Full text available: pdf(1.95 MB) Additional Information: f

Since the early days of logic programming, researchers in the field realized the potential for exploi their referential transparency, among other characteristics, make logic programs interesting candi programming frequently involve irregular computatio ...

Keywords: Automatic parallelization, constraint programming, logic programming, parallelism, pr

63 External memory algorithms and data structures: dealing with **massive data**
Jeffrey Scott Vitter
June 2001 **ACM Computing Surveys (CSUR)**, Volume 33 Issue 2
Full text available: pdf(828.46 KB) Additional Information: f

Data sets in large applications are often too massive to fit completely inside the computers interna as disks) can be a major performance bottleneck. In this article we survey the state of the art in tl reduce the I/O costs. We consider a varie ...

Keywords: B-tree, I/O, batched, block, disk, dynamic, extendible hashing, external memory, hier

64 Compiler-based I/O prefetching for out-of-core applications
Angela Demke Brown, Todd C. Mowry, Orran Krieger
May 2001 **ACM Transactions on Computer Systems (TOCS)**, Volume 19 Issue 2
Full text available: pdf(499.03 KB) Additional Information: f

Current operating systems offer poor performance when a numeric application's working set does

onerous task of rewriting an application to use explicit I/O operations (e.g., read/write). In this pa performance, and requires only minima ...

Keywords: compiler optimization, prefetching, virtual memory

65 Compiler-directed selection of dynamic memory layouts
Mahmut Kandemir, Ismail Kadayif
April 2001 **Proceedings of the ninth international symposium on Hardware/sof**

Full text available: pdf(650.29 KB) Additional Information: f

Compiler technology is becoming a key component in the design of embedded systems, mostly du retargetable compiler optimizations that can be ported across a wide variety of architectures. In p the generated code. Previous compiler-based ap ...

Keywords: array reuse, data dependence, data locality, memory layout optimization, software co

66 Task concurrency management methodology to schedule the MPEG4 IM1 player on a highl
Chun Wong, Paul Marchal, Peng Yang
April 2001 **Proceedings of the ninth international symposium on Hardware/sof**

Full text available: pdf(545.35 KB) Additional Information: f

This paper addresses the concurrent task management of complex multi-media systems, like the I processor platform. Starting from the original "standard" specification, we extract the concurrency this model to improve the task- ...

Keywords: MPEG-4, concurrency, cost-efficiency, embedded system, scheduling

67 A systematic approach to software peripherals for embedded systems
D. Lioupis, A. Papagiannis, D. Psihogiou
April 2001 **Proceedings of the ninth international symposium on Hardware/sof**

Full text available: pdf(562.90 KB) Additional Information: f

The continued growth of microprocessors' performance and the need for better CPU utilization, ha emulate peripherals that, until now, were traditionally implemented in hardware. Software implem cost/performance ratio optimization. We focus on embedd ...

Keywords: embedded processors, reconfigurable architectures, software peripherals

68 Data and memory optimization techniques for embedded systems
P. R. Panda, F. Catthoor, N. D. Dutt, K. Danckaert, E. Brockmeyer, C. Kulkarni, A. Vandercappelle, P
April 2001 **ACM Transactions on Design Automation of Electronic Systems (TOI**

Full text available: pdf(339.91 KB) Additional Information: f

We present a survey of the state-of-the-art techniques used in performing data and memory-relat impact one or more out of three important cost metrics: area, performance, and power dissipation We next cover a broad spectrum of optimizati ...

Keywords: DRAM, SRAM, address generation, allocation, architecture exploration, code transform register file, size estimation, survey

69

A decade of reconfigurable computing: a visionary retrospective
R. Hartenstein
March 2001 **Proceedings of the conference on Design, automation and test in Europe**
Full text available: pdf(768.00 KB) Additional Information: full citation, references, citings, index terms

70 A software engineering perspective on algorithmics
Karsten Weihe
March 2001 **ACM Computing Surveys (CSUR)**, Volume 33 Issue 1
Full text available: pdf(1.62 MB) Additional Information: f

An algorithm component is an implementation of an algorithm which is not intended to be a stand Therefore, the design of algorithm components must also incorporate software-engineering aspect reuse in new, unforseen contex ...

Keywords: algorithm engineering

71 The state of the art in distributed query processing
Donald Kossmann
December 2000 **ACM Computing Surveys (CSUR)**, Volume 32 Issue 4
Full text available: pdf(455.39 KB) Additional Information: f

Distributed data processing is becoming a reality. Businesses want to do it for many reasons, and there (e.g., modern network technology), a number of issues make distributed data processing sti PCs and mainframe server machines; (2) the stat ...

Keywords: caching, client-server databases, database application systems, dissemination-based optimization, replication, wrappers

72 Dynamic scheduling of concurrent tasks with cost performance trade-off
Peng Yang, Dirk Desmet, Francky Catthoor, Diederik Verkest
November 2000 **Proceedings of the 2000 international conference on Compilers, architectur**
Full text available: pdf(175.63 KB) Additional Information: full citation, citings

73 The benefits and costs of DyC's run-time optimizations
Brian Grant, Markus Mock, Matthai Philipose, Craig Chambers, Susan J. Eggers
September 2000 **ACM Transactions on Programming Languages and Systems (TOPLA**
Full text available: pdf(1.59 MB) Additional Information: f

DyC selectively dynamically compiles programs during their execution, utilizing the run-time-comp optimizations are preplanned at static compile time in order to reduce their run-time cost; we call (enabling both single-way and multi ...

Keywords: dynamic compilation, specialization

74 Cellular disco: resource management using virtual clusters on shared-memory multiprocesso
Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
August 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 3
Full text available: pdf(287.05 KB) Additional Information: f

Despite the fact that large-scale shared-memory multiprocessors have been commercially availabl cost of making the required changes to the operating system. A recently proposed approach, calle system technology. In this paper we present a ...

Keywords: fault containment, resource managment, scalable multiprocessors, virtual machines

75 Hardware/software synthesis of formal specifications in codesign of embedded systems
Vincenza Carchiolo, Michele Malgeri, Guiseppe Mangioni
July 2000 **ACM Transactions on Design Automation of Electronic Systems (TOI**

Full text available: pdf(281.08 KB) Additional Information: f

CoDesign aims to integrate the design techniques of hardware and software. In this work, we pres Templated T-LOTOS language to specify the system during all design phases. Templated T-LOTOS temporal ordering in which the events occur from the outside. In this pape ...

Keywords: codesign, embedded system, hardware and software synthesis

76 System-level power optimization: techniques and tools
Luca Benini, Giovanni de Micheli
April 2000 **ACM Transactions on Design Automation of Electronic Systems (TOI**

Full text available: pdf(385.22 KB) Additional Information: f

This tutorial surveys design methods for energy-efficient system-level design. We consider electro consume energy, namely computation, communication, and storage units, and we review method efficient software design and compilation. This survery ...

77 Session summaries from the 17th symposium on operating systems principle (SOSP'99)
Jay Lepreau, Eric Eide
April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2

Full text available: pdf(3.15 MB) Additional Information: full citation, index terms

78 Borrowed-virtual-time (BVT) scheduling: supporting latency-sensitive threads in a general-pu
Kenneth J. Duda, David R. Cheriton
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seven**

Full text available: pdf(1.81 MB) Additional Information: f

Systems need to run a larger and more diverse set of applications, from real-time to interactive tc are specialized to complex real-time paradigms, limiting their applicability to general-purpose syst interactive applications yet weighted sharin ...

79 Cellular Disco: resource management using virtual clusters on shared-memory multiprocess
Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seven**

Full text available: pdf(1.93 MB) Additional Information: f

Despite the fact that large-scale shared-memory multiprocessors have been commercially availabl cost of making the required changes to the operating system. A recently proposed approach, calle system technology.In this paper we present a syste ...

80 Static scheduling algorithms for allocating directed task graphs to multiprocessors
Yu-Kwong Kwok, Ishfaq Ahmad

December 1999 **ACM Computing Surveys (CSUR)**, Volume 31 Issue 4

Full text available: pdf(723.58 KB) Additional Information: f

Static scheduling of a program represented by a directed task graph on a multiprocessor system t NP-complete problem in general, researchers have resorted to devising efficient heuristics. A pleth programming, searching, graph-theory, randomization, genetic ...

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Keywords: code placement, conflict misses, temporal profiling, working-set optimization

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Customizing the protocols that manage accesses to different data structures within an applicatior protocols are hard to use directly because the mechanisms they provide for manipulating protocc protocols. We describe the design and implementat ...

Keywords: parallel processing

84 Java annotation-aware just-in-time (AJIT) complilation system
Ana Azevedo, Alex Nicolau, Joe Hummel
June 1999 **Proceedings of the ACM 1999 conference on Java Grande**

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85 Eliminating synchronization overhead in automatically parallelized programs using dynamic
Pedro C. Diniz, Martin C. Rinard
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Keywords: parallel computing, parallelizing compilers

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Code analysis tools provide support for such software engineering tasks as program understandir generators such as Aria and GEN++ which have been used to generate a wide range of practical framework that allow it to be ...

Keywords: code inspection, metrics, reverse engineering, source analysis

87 Provably efficient scheduling for languages with fine-grained parallelism
Guy E. Blelloch, Phillip B. Gibbons, Yossi Matias
March 1999 **Journal of the ACM (JACM)**, Volume 46 Issue 2

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Many high-level parallel programming languages allow for fine-grained parallelism. As in the popı program without specifying the mapping of program tasks to processors. A common concern in e the amount of space (memory) needed. Without caref ...

88 Broadcast protocols to support efficient retrieval from databases by mobile users
Anindya Datta, Debra E. VanderMeer, Aslihan Celik, Vijay Kumar
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Mobile computing has the potential for managing information globally. Data management issues posed as an important probllem. Such protocols are employed by database servers to decide on t protocols and also propose efficient retrieval s ...

Keywords: adaptive broadcast protocols, client-server computing, energy conservation, mobile

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We report on a detailed study of the application and effectiveness of program analysis based on a strict independence. We first propose and prove correct a methodology for the application in the in the sense of allowing the use of dif ...

Keywords: abstract interpretation, automatic parallelization, data flow analysis, logic programm

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Kathleen Knobe, James M. Rehg, Arun Chauhan, Rishiyur S. Nikhil, Umakishore Ramachandran
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Keywords: Pthreads, dynamic scheduling, irregular parallelism, lightweight threads, multithread

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Soichiro Araki, Angelos Bilas, Cezary Dubnicki, Jan Edler, Koichi Konishi, James Philbin
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Keywords: Active Messages (AM), Basic Interface for Parallelism (BIP), Fast Messages (FM), Loc computing, latency, performance analysis, user-space communication

96 Space/time-efficient scheduling and execution of parallel irregular computations
Tao Yang, Cong Fu
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Keywords: DAG scheduling, direct remote memory access, irregular parallelism, run-time suppc

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Ken Kennedy, Ulrich Kremer
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Keywords: high performance Fortran

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Navin Kabra, David J. DeWitt
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David B. Skillicorn, Domenico Talia
June 1998 **ACM Computing Surveys (CSUR)**, Volume 30 Issue 2

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